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Phenomenon	Possible Causes	Solutions
The output voltage is lower than rating	 Start-up failure resulted from insufficient input power Low input voltage Large resistance of input filtering inductor, or improper fuse used Large wire loss caused by very thin input lead wire No capacitors are connected at th e two ends of linear regulators (78L05 etc) or connected at the wrong position. Being a large voltage drop caused by forward linear regulator A large voltage drop of diode to block reverse current at the input end. 	 Use a higher-power input power supply instead. Adjust the input voltage into the recommend input voltage range. Reduce the filtering inductance or resistance of inductor, or reduce DCR of fuse. Enlarge the sectional area of lead or shorten lead length to reduce resistance. Connect sufficient capacitors closely to the two ends of linear regulators. Use LDO linear regulators to reduce the voltage drop. Use a diode with low voltage drop instead.
	 Over-current in output circuit Too large an output external filtering capacitor 	 Check the external output circuit. If it consumes power over rating or is short-circuited, use a higher output power MORNSUN DC-DC converter module instead. The external output capacitor should be lower than maximum value showing on the datasheet. Capacitance is determined by the output current, according to the principle 1uF/100mA. (It's not suggested to connect external capacitors when the output power is lower than 0.5W and output voltage is higher than 20V.). If the output ripple is still requested to be lower, connect an LC filtering circuit at the output end. (Inductance: 4.7-10uH).
Output voltage is higher than rating	 The input voltage is too high Output end is disconnected or without load The load is lower than 10% of rated load 	Adjust the input voltage into the recommend input voltage range Make sure 8% of rated load is connected to the output end when operating. If there is no load in the application, connect in parallel 8% of rated load at the output end to ensure the load of module is up to 10% minimum.
Fails after a certain period of operation	 Over-voltage at the input end Over-voltage at the input end No capacitors are connected at the two ends of linear regulators (78L05 etc) or connected at wrong position. Iow input voltage Defectively soldered at the input end The module operates under no load or the load is lower than 10% of rated load (large power margin) Too large an output external filtering capacitor The breakdown voltage of output external capacitor has a critical margin Overload at the output end 	 Connect a TVS in parallel at the input end of module Connect sufficient capacitors closely to the two ends of linear regulators. Adjust the input voltage into the recommend input voltage range Make sure the soldering is well. Make sure 10% of rated load is connected to the output end when operating. If there is no load or the load is too light in the application, connect in parallel 5% of rated load at the output end to ensure the load of module is no less than 10% minimum (or replace with a lower power module). The external output capacitor should be lower than maximum value showing on the datasheet. Capacitance is determined by the output current,

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Phenomenon	Possible Causes	Solutions
	5. Ambient temperature is too high or	according to the principle 1uF/100mA. (It's not
	bad cooling	suggested to connect external capacitors when the
	6. Defectively soldered at the input	output power is lower than 0.5W and output
	end	voltage is higher than 20V.).
		3. The breakdown voltage of the capacitor must be
		no less than 1.5 times of the highest actual voltage
		in the circuit.
		4. The actual load should be lower than 100% of
		rated load power.
		5. Make sure the ambient temperature is not higher
		than 85° C. (For an application of ambient
		temperature constantly at 70° C or above, please
		contact us to custom a high temperature
		product)
		6. Make sure the soldering is well.
	1. Reverse polarity connection	1. Connect a diode with low voltage drop to block
	2. Output voltage is much higher than	reverse current at the input end.
	rating	2. Adjust the input voltage into the recommend
	3. Reversed output capacitors	input voltage range
	connection	3. Check phase of output capacitor
		1. The external output capacitor should be lower than
		maximum value showing on the datasheet. Capacitance is determined by the output current,
		according to the principle 1uF/100mA. (It's not
	1. External output capacitor is too large	suggested to connect external capacitors when the
	or the breakdown voltage of the	output power is lower than 0.5W and output
	capacitor is insufficient	voltage is higher than 20V.). If the output ripple is
1. The module is	2. The output circuit is vulnerable to	still requested to be lower, connect an LC filtering
destroyed when powering.	short-circuit	circuit at the output end. (Inductance: 4.7-10uH)
2. The damage	3. no load at the output end of SMD	The breakdown voltage of the capacitor must be
rate is high when powering	package module	no less than 2 times of the highest actual voltage in
when powering		 Connect a short-circuit protection circuit at the
		2. Connect a short-circuit protection circuit at the output end
		3. Add at lease 1% of rated load at the output end
		6.For SMD products:
	1 Eon SMD nuclusta	7. The temperature of the reflow can not be higher
	 1.For SMD products: 2. The temperature of the reflow is too 	than the recommendation by datasheet.
	high.	8. The gas phase reflow temperature is lower 10° C
	3. Use gas phase reflow.	than heat convection reflow.9.Process reflow soldering more than 2 times is not
	4. Process reflow soldering many times.	allowed.
	5. Use heat gun when maintain.	10.Do not use hot gun when maintain and the control
		the temperature ($<300^{\circ}$ C) the time (<10 S).
1. The module		
becomes too		
hot.		
2. Fails after	1. The input voltage is too high	1. adjust the input voltage into the recommend input
burin-in.	2. No capacitors are connected at the two	voltage range
3. Some fluidity	ends of linear regulators (78L05 etc) or	2. Connect sufficient capacitors closely to the two
	connected at the wrong position. 3. Low input voltage.	ends of lineauregula的所区车陂路黄洲工业区六栋 (510660)
Page 2 of 4	4. Defectively soldered at the input end	 Adjust the influence of the
	, r , , , , , , , , , , , , , , , , , ,	input Volta 210, 38601850 38601530 FAX: (+86)20-38601272

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Phenomenon	Possible Causes	Solutions
		4. Make sure the soldering is well.
overflows from the module after burn-in.	 The module operates under no load or the load is lower than 10% of rated load Too large an output external filtering capacitor The breakdown voltage of output external capacitor has a critical margin Overload at the output end Ambient temperature is too high or bad cooling Defectively soldered at the input end 	 Make sure 10% of rated load is connected to the output end when operating. If there is no load or the load is too light in the application, connect in parallel 5% of rated load at the output end to ensure the load of module is no less than 10% minimum. The external output capacitor should be lower than maximum value showing on the datasheet. Capacitance is determined by the output current, according to the principle 1uF/100mA. (It's not suggested to connect external capacitors when the output power is lower than 0.5W and output voltage is higher than 20V.). The breakdown voltage of the capacitor must be no less than 2 times of the highest actual voltage in the circuit. The actual load should be lower than 100% of rated load power. Make sure the ambient temperature is not higher than 85°C, for constant high temperature application Make sure the soldering is well.
The output noise is quite large or the whole system is noise susceptible	 The distance between the module and noise susceptible components(A/D, D/A or MCU) in the main circuit is too critical. No decoupling capacitors are connected to the input end of noise susceptible components in the main circuit. Beat interference generated by separate single output modules in the multi-channel system. Earth wire is mishandled. The output noise interferes normal operation when the above problem doesn't exist. 	 Make sure the soldering is well. Separate the module from the module and noise susceptible components in the main circuit as much as possible or isolate the module and noise susceptible components in the main circuit. Connect a 0.1µF decoupling capacitor at the input end of noise susceptible components (A/D, D/A or MCU) in the main circuit. Replace a few single output modules with one MORNSUN multi-channel output module so as to eliminate interference. Adopt remote one point earth. Use MORNSUN fixed input, isolated and regulated output series products instead.
 Poor anti-interferenc performance Poor EMI characteristics. 	 Poor anti-common-mode-interference performance No shielding No safety capacitors are connected to the unit. 	 Connect a common mode choke and TVS at the input end. Shield the surface with metal entirely. Connect a 47-100pF safety capacitor between GND and (The breakdown voltage is determined by the actual demand, generally 1000-3000VDC).



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Phenomenon	Possible Causes	Solutions
Output ripple shows fuzzy on the oscilloscope	The oscillograph's grounding line is not well connected	Connect the grounding line well.

NOTE: Any other question, please feel free to contact our FAE department. Tel: 0086-20-38601850 Fax: 0086-20-38601272 Email: <u>FAE@mornsun.cn</u>